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- The suggested length for each chapter abstract is 100-150 words
- It should provide a brief review of the main content and theme of the chapter
- Do not refer to any topics or material is not included in the chapter being abstracted
- Avoid non-standard language, terms and abbreviations if possible, and always define abbreviations at their first mention in the abstract itself.

Include the abstract in your manuscript at the start of each chapter, under the heading 'Non-print items' (see example below). This heading is used to indicate to the typesetter that the abstract is not to be included in the printed edition of your work.

5. Guidelines for preparing a chapter keyword list

A keyword is any significant word or phrase used to describe the contents of a chapter, or a significant or descriptive word or phrase used as a reference point for finding information on the topic of the chapter.

- Provide 5—10 keywords for each chapter
- Use American spelling

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If you have any queries on the writing of abstracts or key words please contact Elsevier.

Example of Abstract and Keywords provided in manuscript form

CHAPTER 3

EPS Architecture Overview

NON-PRINT ITEMS

Abstract

This chapter introduces the mobile broadband architecture known as EPS (Evolved Packet System). It starts by presenting a high level perspective of the complete architecture as defined in the SAE (Systems Architecture Evolution) work item. It then explains the operation of logical nodes and functions in the network. By the end of the chapter the main elements of the EPS architecture, including the circuit core domain, user management domain, and internet protocol multimedia system domain, are explained. A full discussion of core network element functions, interfaces and signal flows is included to show their relationship with the key concepts of session management and mobility, security, quality of service, and selection functions, and with EPS network entities, protocols and procedures.

Key Words

Evolved Packet System Architecture; EPS; Systems Architecture Evolution; SAE; Circuit core domain; logical nodes; multimedia system domain

Chapter starts here

3.1 There are several domains in the EPS, each one... etc.